

0/009594  
Translation of International Application  
Attorney Docket No. 951/50636

11/PRTS

WO 01/35108

PCT/EP00/09798

Input Circuit for Inductive Speed Sensor

The invention relates to an input circuit for an inductive speed sensor according to the preamble of claim 1.

Many internal combustion engines use transmitter wheels with inductive sensors to determine, for example, the position of the crankshaft. Not only are inductive sensors of this type very rugged and usable at very high temperatures, they are also extremely inexpensive. However, the amplitude of the signal depends on the speed, covering a range from a few millivolts to more than 100 volts. Electronic circuits are normally used in order to be able to detect low amplitudes at low speeds, on the one hand, while reaching, on the other hand, the highest possible level of noise immunity during normal engine operation, i.e. in the presence of high amplitudes. The electronic circuits either divide down the existing sensor voltage in one or more stages, or they switch the switching thresholds of evaluating comparators over. Both of the above-named methods measure the average level of the sensor voltage and effect the switch-over of their evaluation dependent on the latter.

Translation of International Application  
Attorney Docket No. 951/50636

It is disadvantageous that this process necessarily involves a relatively high level of complex circuitry in order to determine the average amplitude of the signal, to switch-over the thresholds or voltage dividers, to provide a hysteresis for the switch-over and to prevent undesirable additional edges that may occur in the more sensitive area during a switch-back. Therefore, specially designed and costly so-called ASICs are often used.

It is the subject-matter of the present invention to provide an input circuit of the kind described at the outset that can be used to achieve a high degree of input sensitivity during the start-up phase and a good signal-to-noise ratio during normal engine operation utilizing the simplest means.

This objective is achieved with the characteristics specified in claim 1.

A key concept of the invention provides that a voltage divider of a signal amplitude is no longer switched over on the basis of an average sensor signal value but on the basis of the speed. A precise analysis of the above-mentioned problem revealed that the low amplitudes occur, for the most part, only during the start-up process, i.e. when the engine starter rotates at less than 100 rpm. But once the engine starts, the idling speed is reached within a very short time. Idling speeds, however, are within a range of approximately 500 to

Translation of International Application  
Attorney Docket No. 951/50636

1,000 rpm. At this speed, the amplitude of the transmitter signal reached approximately 10 times the initial amplitude. The amplitude, in turn, changes at a maximum factor 10 across the entire remaining speed range. The invention takes advantage of the dependence of the signal amplitude on speed.

Specifically, a micro-controller can provide the speed to the engine control, where this dimension is already present. The speed thresholds and the switching hysteresis are also easily adjustable with the present invention.

Furthermore, if taking into account that the initial sensitivity level is only necessary during the start-up phase, a simple switch-over threshold will be enough to ensure a sufficient noise immunity during normal engine operation.

Other characteristics are defined in the sub-claims.

In the following, the invention will be described in greater detail utilizing a special embodiment and in reference to the single attached drawing.

The single drawing depicts an input circuit according to the invention that is comprised of two circuit inputs (or connector pins) 1, 2, to which an inductive transmitter (not shown here) can be connected. The two circuit inputs 1 and 2 are loaded via a resistor R1.

Moreover, the circuit input 2 is connected to a voltage divider, comprised of the resistors R2 and R3, in such a way that the resistor R2 connects the circuit input 2 to the ground, and the resistor R3 connects the circuit input 2 to a constant voltage of 5V. The reference level of the transmitter is raised by way of the voltage divider, which is comprised of the resistors R2 and R3; as a result, it is possible to detect negative amplitudes using a comparator K.

The circuit input 1 is connected to a first input (-) of the comparator K via a resistor R4. Two Zener-diodes D1 and D2, wired opposite in relation to each other, are arranged between the resistor R4 and the second circuit input 2; in conjunction with the resistor R4, they protect the comparator K from an input voltage that is too high.

The second input (+) of the comparator K is connected to the circuit input 2 via a resistor R6. Also, the second input (+) of the comparator K is also connected to the output of the comparator K via a resistor R7. The two resistors R6 and R7 define a switching hysteresis.

The output of the comparator K is connected to a micro-controller M (input E) that is used to evaluate the transmitter signal. The micro-controller M, in turn, uses this transmitter information to control the engine.

Another resistor R5 and two p-channel MOS [metal-oxide semiconductor] field-effect transistors T1 and T2 are wired between the resistor R4 and the circuit input 2. Consequently, the first input (-) of the comparator K is connected via the resistor R4 to the circuit input 1 and via the combination of the resistor R5 and the two MOS field-effect transistors T1 and T2 to the circuit input 2. Using the combination of the components R4, R5, T1 and T2, it is possible to realize a switchable voltage divider and, therefore, a controllable amplitude reduction at the comparator K. The two MOS field-effect transistors T1 and T2 are necessary because of the negative sensor voltages; and they are both arranged in series and aligned in different switching directions. The inputs of the two MOS field-effect transistors T1 and T2 are connected to an output A of the micro-controller M and controlled by the latter. In the present case, the inductive sensor supplies +/- 1.3 volts at approximately 100 rpm. At 1,000 rpm it generates +/- 12.7 volts. The switching threshold of the comparator is at approximately +/- 1.2 volts. If resistance values of 51.1 k $\Omega$  are used for R4 and of 11.5 k $\Omega$  for R5, the resulting switching thresholds are higher by a factor of 5, i.e. approximately +/- 6 volts. This threshold provides a good signal-to-noise ratio.

If the vehicle is started up and current is supplied to the micro-controller M, the micro-controller M initially switches the two MOS field-effect transistors T1 and T2 via its output pin A to a high-impedance state. Thus, the sensor signal originating from the (not shown) inductive sensor or transmitter is applied, undamped, at the comparator K. High amplitudes, however, are limited by way of the two diodes D1 and D2.

The micro-controller M evaluates the digitized signal coming from the comparator K and emits a signal if the established speed threshold is exceeded. This causes the micro-controller M to switch the MOS field-effect transistors T1 and T2 to a low-impedance level allowing for the sensor signal at the comparator K to be reduced by the then-active voltage divider consisting of the resistors R4 and R5. The speed determination is now less sensitive with respect to noise in the sensor signal. The resistors - as mentioned above - are selected in such a way that the signal level at the comparator K is sufficient for safe switching even under the poorest conditions. It is beneficial if the change-over speed is below the no-load speed in order to prevent any back and forth switching while the engine is running.

The present invention ensures a high input sensitivity during the start-up phase and a good signal-to-noise ratio during engine operation using the simplest means. Significant cost

Translation of International Application  
Attorney Docket No. 951/50636

savings are realized in comparison to a conventional solution  
that uses a so-called ASIC.